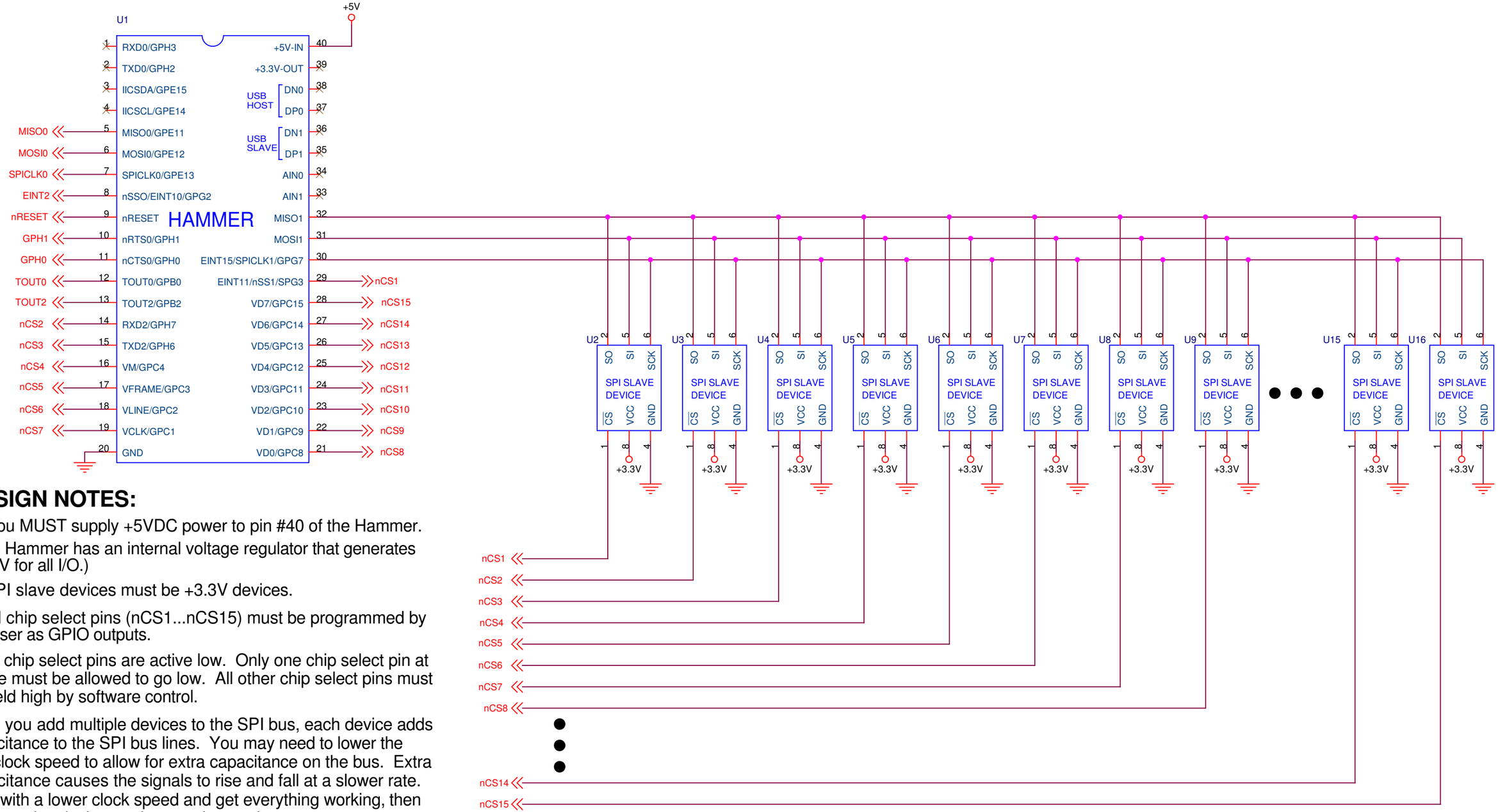
INTERFACING THE HAMMER TO MULTIPLE SPI SLAVE DEVICES



DESIGN NOTES:

1) You MUST supply +5VDC power to pin #40 of the Hammer.
(The Hammer has an internal voltage regulator that generates +3.3V for all I/O.)

2) SPI slave devices must be +3.3V devices.

3) All chip select pins (nCS1...nCS15) must be programmed by the user as GPIO outputs.

4) All chip select pins are active low. Only one chip select pin at a time must be allowed to go low. All other chip select pins must be held high by software control.

5) As you add multiple devices to the SPI bus, each device adds capacitance to the SPI bus lines. You may need to lower the SPI clock speed to allow for extra capacitance on the bus. Extra capacitance causes the signals to rise and fall at a slower rate. Start with a lower clock speed and get everything working, then increase the clock speed to test the maximum rate.

6) This examples uses SPI Port #1. You may use SPI Port #0 instead. Or you could put half of the devices on SPI Port #0 and the other half of the devices on Port #1.

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